

Amendments to the Claims

This listing will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently amended) A method for reading and storing data by means of a direct memory access (DMA) medium, comprising the steps of:
deciding a shift direction and a predetermined number of bits to be shifted in advance when a request is made so that data read from a first storage medium can be processed; and
sequentially storing bit strings configuring the read data in a DMA control register, shifting the bit strings by the predetermined number of bits in the decided shift direction, and transferring the shifted bit strings to a second storage medium.
2. (Original) The method as set forth in claim 1, wherein each of the bit strings configuring the read data is configured by one of an 8-bit string, a 16-bit string or a 32-bit string.
3. (Original) The method as set forth in claim 2, wherein the number of bits to be shifted has a value of from 0 to 7.
4. (Original) The method as set forth in claim 3, wherein the step of deciding the shift direction and the number of bits to be shifted depends upon bit values set by the DMA medium.
5. (Original) The method as set forth in claim 1, further comprising the steps of:
classifying the bit strings configuring the read data into more significant bit strings and less significant bit strings; and
rearranging positions of less and more significant bit strings and writing the read data to the second storage medium according to a result of the rearrangement.

6. (Original) The method as set forth in claim 5, wherein the read data is configured in the form of 32 bits at the step of rearranging the positions of the less and more significant bit strings.

7. (Original) The method as set forth in claim 6, wherein the step of rearranging the positions of the less and more significant bit strings depends upon bit values set by the DMA medium.

8. (Currently amended) An apparatus for reading and storing data by means of a direct memory access (DMA) medium, comprising:

a first storage medium for storing data read in a source address;

the DMA medium for deciding a shift direction and a predetermined number of bits to be shifted in advance when a request is made so that the read data can be processed, sequentially storing bit strings configuring the read data in a DMA control register, shifting the bit strings by the predetermined number of bits in the decided shift direction, and transferring the shifted bit strings; and

a second storage medium for storing data transferred from the DMA medium.

9. (Original) The apparatus as set forth in claim 8, wherein the DMA medium reads each of the bit strings configuring the read data with one of an 8-bit string, a 16-bit string or a 32-bit string.

10. (Original) The apparatus as set forth in claim 9, wherein the DMA medium carries out a shift operation according to the number of bits to be shifted that has a value of from 0 to 7.

11. (Original) The apparatus as set forth in claim 10, wherein the DMA medium decides the shift direction and the number of bits to be shifted according to set bit values.

12. (Original) The apparatus as set forth in claim 8, wherein the DMA medium classifies the bit strings configuring the read data into more significant bit strings and less significant bit strings, rearranges positions of less and more significant bit strings, and writes the read data to the second storage medium according to a result of the rearrangement.

13. (Original) The apparatus as set forth in claim 12, wherein the DMA medium rearranges the positions of the less and more significant bit strings when the read data is configured in the form of 32 bits.

14. (Original) The apparatus as set forth in claim 13, wherein DMA medium rearranges the positions of the less and more significant bit strings according to set bit values.

15. (New) A method for transmitting data in a mobile communication system, the method comprising:

- at a medium access control (MAC) layer, receiving at least one radio link control (RLC) data packet from a RLC layer and storing said at least one RLC data packet in a MAC layer memory by shifting said at least one RLC data packet by as many bits as indicated by a MAC layer header;

- adding the MAC layer header in front of said at least one RLC layer data packet to make a MAC layer data packet; and

- transferring at least one MAC layer data packet to a physical (PHY) layer,

- wherein said MAC layer header includes control information for said MAC layer data packet.

16. (New) The method of claim 15, further comprising:

- at said PHY layer, receiving said at least one MAC layer data packet and storing said at least one MAC layer data packet in a PNY layer memory; and

adding a cyclic redundancy checking (CRC) at the rear of said at least one MAC layer data packet to make a PHY layer data packet.

17. (New) The method of claim 15, wherein said data transfer between layers is performed by direct memory access (DMA).

18. (New) The method of claim 17, wherein the method of transmitting data further comprises:

deciding a shift direction and a size of bits to be shifted according to information in a header;

receiving a data string from a first memory and storing said data string in a DMA memory by shifting according to said decided shift direction and said size of bits; and

sequentially transferring said stored data string to a second memory.